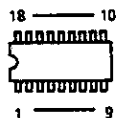
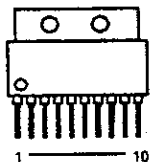


SOLID STATE DEVICE BASE DIAGRAM

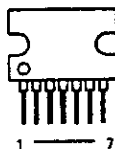
RH-iX0711CEN1



RH-iX0250CEZZ



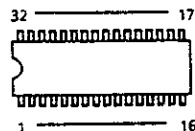
RH-iX0238CEZZ



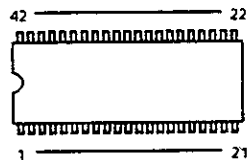
RH-iX1118CEZZ



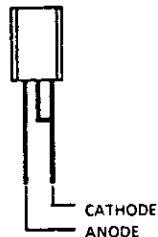
RH-iX0712CEN1



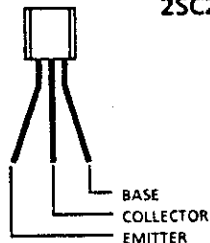
RH-iX1147CEZZ



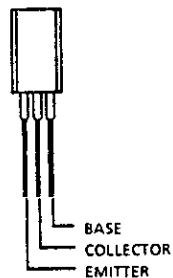
RH-iX0037CEZZ



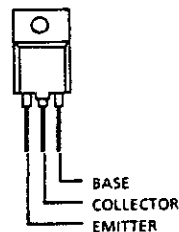
2SA1015(Y)
2SA950
2SC1906
2SC3279
2SC383
2SC945



2SC2236
2SC2229



2SD1554




DESCRIPTION OF SCHEMATIC DIAGRAM

SAFETY NOTE:

1. DISCONNECT THE AC PLUG FROM THE AC OUTLET BEFORE REPLACING PARTS.
2. SEMICONDUCTOR HEAT SINKS SHOULD BE REGARDED AS POTENTIAL SHOCK HAZARDS WHEN THE CHASSIS IS OPERATING.

IMPORTANT SAFETY NOTICE:

1. PARTS MARKED WITH "△" () ARE IMPORTANT FOR MAINTAINING THE SAFETY OF THE SET. BE SURE TO REPLACE THESE PARTS WITH SPECIFIED ONES FOR MAINTAINING THE SAFETY AND PERFORMANCE OF THE SET.

NOTE:

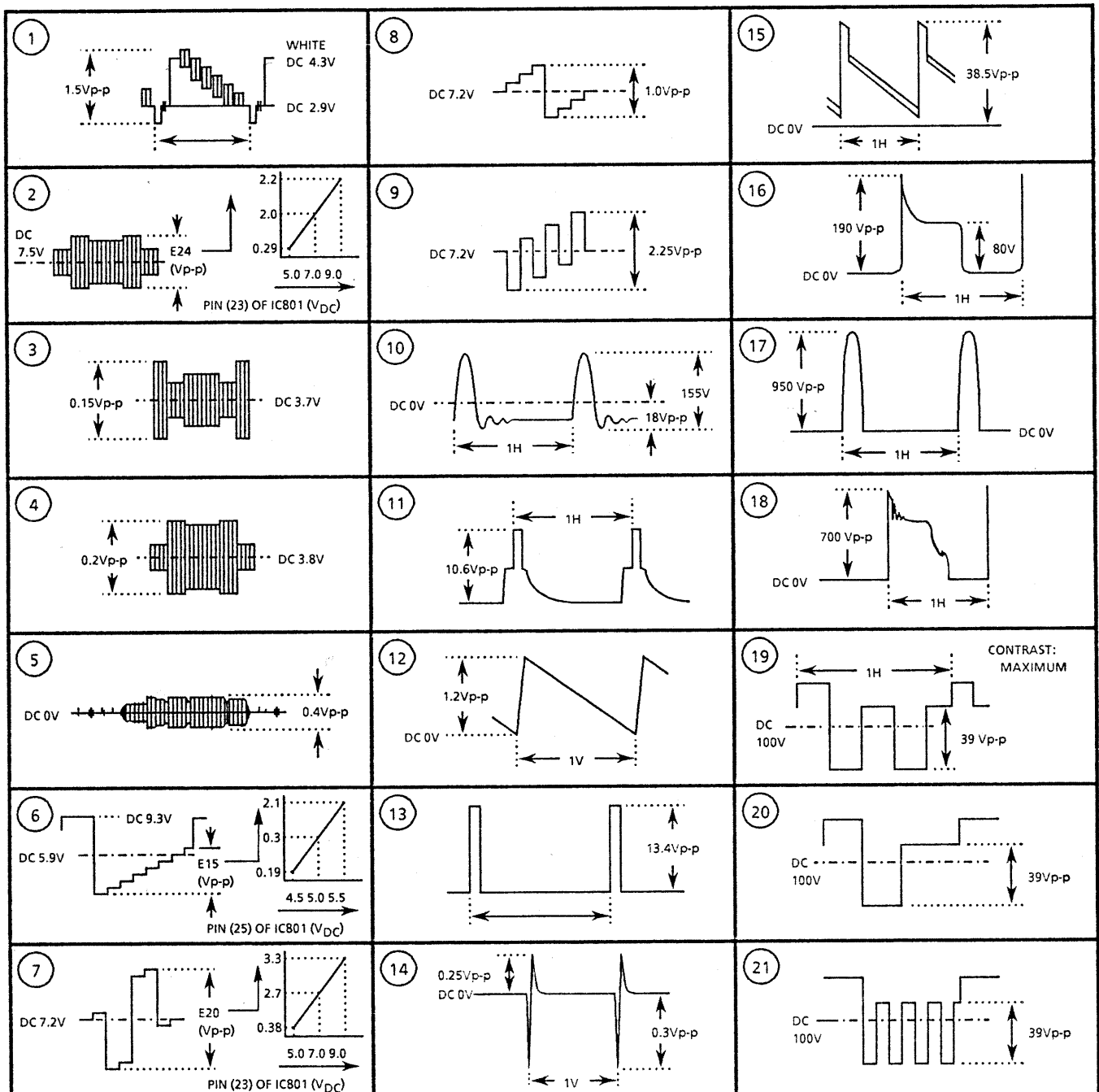
1. The unit of resistance "ohm" is omitted ($K = 1000$ ohms, $M = 1$ Meg ohm).
2. All resistors are 1/8 watt, unless otherwise noted.
3. All capacitors are μF , unless otherwise noted $P = \mu F$.

VOLTAGE MEASUREMENT CONDITIONS:

1. Voltages in parenthesis measured with no signal.
2. Voltages without parenthesis measured with 3000 μV B & W or Colour signal.
3. All the voltages in each point are measured with VTVM.

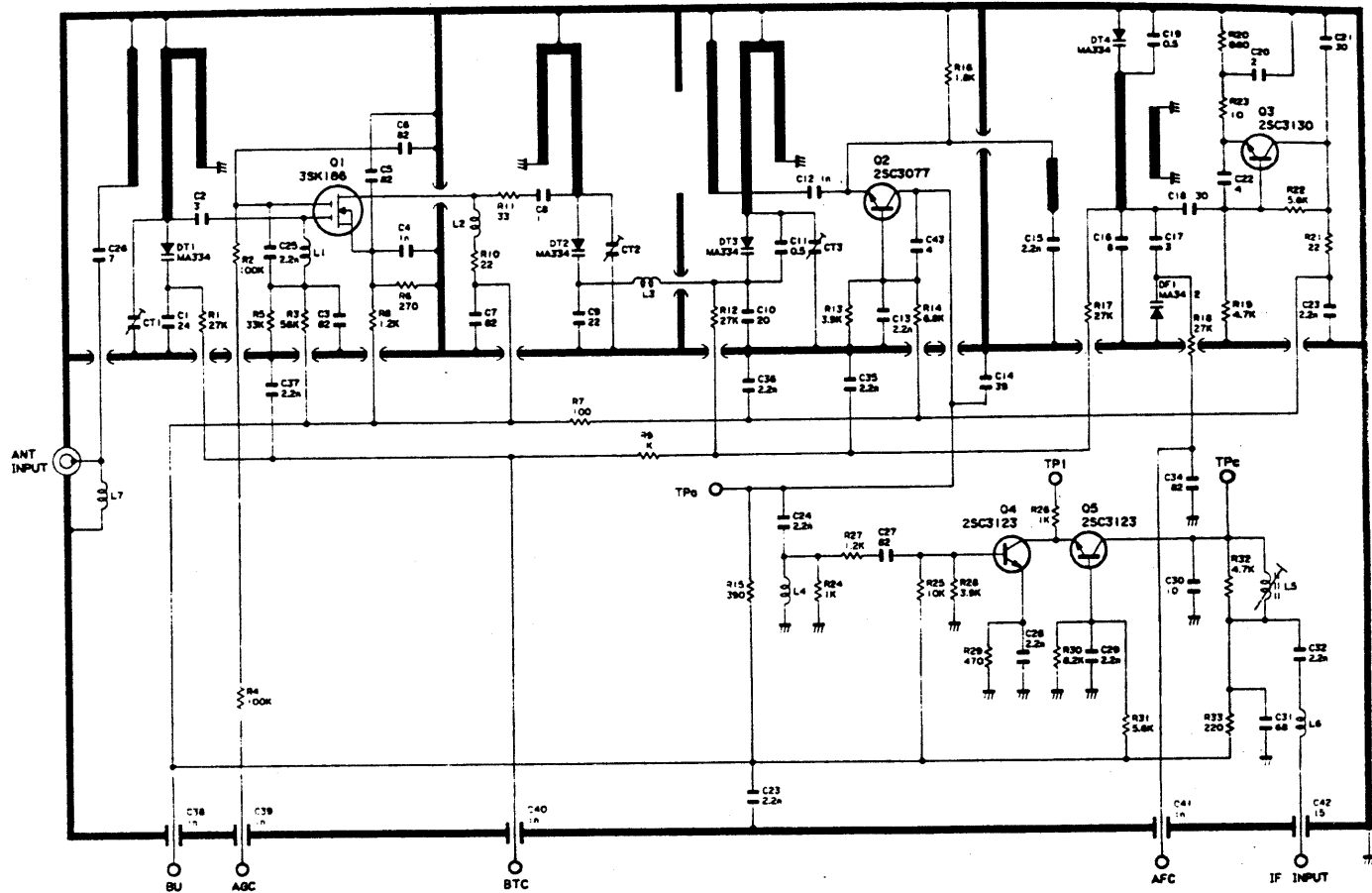
WAVEFORM MEASUREMENT CONDITIONS:

1. Colour bar generator signal of 2.3 V peak to peak applied at Base of Video Buffer Amp. Q202.
2. Approximately 4.0 V AGC bias.



VTUITS7B////

TUNER



CAUTION:
This circuit diagram is original one. Therefore there may be slight differences from yours.

